Curriculum Vitae

Kevin Bolding
Associate Professor of Electrical Engineering
Director of Engineering and Computer Science Programs
Chair of the Electrical Engineering Department

EDUCATIONAL BACKGROUND

Ph.D. in Computer Science and Engineering, University of Washington, August 1993. Dissertation title: Chaotic Routing - Design and Implementation of an Adaptive Multicomputer Router. Supervised by Prof. Lawrence Snyder.

M.S. in Computer Science and Engineering, University of Washington, January 1991. Supervised by Prof. Lawrence Snyder.

B.A. in Computer Science and Mathematical Sciences, Rice University, 1988. Magna cum laude.

SERVICE AT SEATTLE PACIFIC UNIVERSITY

2012-pres.   Director of Engineering and Computer Science Programs, Seattle Pacific University

2005-pres.   Chair of the Electrical Engineering Department, Seattle Pacific University

2002-pres.   Associate Professor of Electrical Engineering, Seattle Pacific University

1998-2002   Assistant Professor of Electrical Engineering, Seattle Pacific University

1995-1998   Assistant Professor of Engineering, Seattle Pacific University

OTHER RELATED EXPERIENCE

2009   PSoC Microcontroller Training instructor for Cypress Semiconductor.

1993-1996   Research Associate, Dept. of Computer Science and Engineering, University of Washington, Seattle, WA.
Developed a powerful network simulator and co-authoring nearly a dozen conference and journal articles on network routing, and VLSI design and simulation.

CONSULTING AND PATENTS

2002-2004   Expert evaluator for Foundation for International Services, Inc.

PRINCIPAL PUBLICATIONS (within the past five years)

David A.Wicks, Baine B. Craft, Geri N. Mason, Kristine Gritter, Kevin Bolding. "An investigation into the community of inquiry of blended classrooms by a Faculty Learning Community." *Internet and Higher Education* 25 (2015): 53-62.

Wicks, David, Kevin Bolding, Erla Champ-Gibson, Baine Craft, Kristine Gritter, Geri Mason . "BL2: A Blended Learning Learning Community for Curriculum and Faculty Development." 9th Annual Sloan Consortium Blended Learning Conference. Milwaukee, WI. 2012.

PROFESSIONAL SOCIETIES

Senior Member of IEEE

HONORS AND AWARDS

1990   Intel Fellowship, University of Washington. Worked on network routers, and developed a VLSI chip that provided the advanced features of chaotic routing with the raw performance of simpler deterministic routers.

INSTITUTIONAL AND PROFESSIONAL SERVICE (within the past five years)

Member, Seattle Pacific University Assessment Committee, since 2013

Member, Seattle Pacific University Undergraduate Policies and Evaluation Committee, 2010-2011